MPS6560

Audio Transistor

NPN Silicon

Features

• Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	25	Vdc
Collector - Base Voltage	V _{CBO}	25	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	I _C	500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

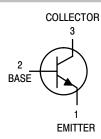
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. $R_{\theta,JA}$ is measured with the device soldered into a typical printed circuit board.



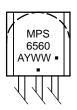
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MARKING DIAGRAM





MPS6560 = Device Code A = Assembly Location

Y = Year WW = Work Week ■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MPS6560	TO-92	5,000 Units/Box
MPS6560G	TO-92 (Pb-Free)	5,000 Units/Box

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MPS6560

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

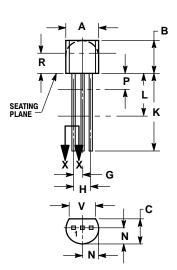
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	·			
Collector – Emitter Breakdown Voltage (Note 2) (I _C = 10 mAdc, I _B = 0)	V _(BR) CEO	25	_	Vdc
Collector – Base Breakdown Voltage (I _C = 100 µAdc, I _E = 0)	V _(BR) CBO	25	_	Vdc
Emitter – Base Breakdown Voltage (I _E = 100 µAdc, I _C = 0)	V _{(BR)EBO}	5.0	_	Vdc
Collector Cutoff Current (V _{CE} = 25 Vdc, I _B = 0)	I _{CES}	-	100	nAdc
Collector Cutoff Current (V _{CB} = 20 Vdc, I _E = 0)	I _{CBO}	-	100	nAdc
Emitter Cutoff Current $(V_{EB(off)} = 4.0 \text{ Vdc}, I_C = 0)$	I _{EBO}	-	100	nAdc
ON CHARACTERISTICS (Note 2)	·			
DC Current Gain $(I_C = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_C = 500 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	h _{FE}	35 50 50	- - 200	-
Collector – Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc)	V _{CE(sat)}	-	0.5	Vdc
Base – Emitter On Voltage (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)	V _{BE(on)}	-	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS	·			
Current-Gain — Bandwidth Product (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 20 MHz)	f _T	60	_	MHz
Output Capacitance $(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{obo}	-	30	pF

^{2.} Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.

MPS6560

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 114-3M, 1902.
 CONTROLLING DIMENSION: INCH.
 CONTOUR OF PACKAGE BEYOND DIMENSION R
 IS UNCONTROLLED.
 LEAD DIMENSION IS UNCONTROLLED IN P AND
- BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
v	0.135		3 43	

STYLE 1:

PIN 1. EMITTER

BASE 2.

COLLECTOR

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